



2838

Attorney's Docket No. 21891.03100 - PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:  
Shauki Elassaad et al.

Examiner: Not yet assigned

For: INSERTION OF REPEATERS  
WITHOUT TIMING  
CONSTRAINTS

Group Art Unit: Not yet assigned

Serial No.: 09/919,740

Filed: August 1, 2001

---

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Applicant submits herewith patents, publications or other information (listed on the attached Form PTO-1449) of which Applicant is aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

In accordance with 37 CFR § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in this statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b).

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is

submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

No fee is believed to be due at this time, however, the Commissioner is hereby authorized to charge any additional fees (or credit any overpayment) associated with this communication and which may be required under 37 CFR § 1.16 or § 1.17 to **Deposit Account No. 502-603**, referencing Attorney Docket No. **351891.03100**. A duplicate sheet is attached.

Respectfully submitted,

Reed Smith Crosby Heafey LLP  
Professional Corporation

Dated: 11/14/2003

By: 

Name: John W. Carpenter  
Registration No. 39,129  
Attorneys for Applicants

Two Embarcadero Center, Suite 2000  
PO Box 7936  
San Francisco, CA 94120-7936  
**Direct Dial (415) 659-5927**  
(415) 543-8700 Telephone  
(415) 391-8269 Facsimile  
Attached: Form PTO-1449

Please type a plus sign (+) inside this box → +

Substitute for Form PTO-1449

**Complete if known**

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet **1** of **1**

Application Number	09/919,740
Filing Date	08/01/2001
First Named Inventor	Shauki Elassaad et al.
Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	21891.03100

## **U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant Of Cited Document	Date of Publication of Cited Document MM- DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

## **OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T*
	<b>C1</b>	Otten, R. et al. (1998) "Planning for Performance" <i>Proceedings of the 35<sup>th</sup> Design Automation Conference</i> , San Francisco	
	<b>C2</b>	van Ginneken, L.P.P.P. (1990) "Buffer Placement in Distributed RC-tree Networks for Minimal Elmore Delay," <i>Proceedings of the International Symposium on Circuits and Systems</i> pp. 865-868	
	<b>C3</b>	Murgai, R. (1999) "Delay-Constrained Area Recovery Via Layout-Driven Buffer Optimization," <i>Proceedings of the International Workshop on Logic Synthesis</i> , Lake Tahoe	
	<b>C4</b>	Executive Summary from <a href="http://www.gigascale.org/about/overview/executive-summary.htm">www.gigascale.org/about/overview/executive-summary.htm</a>	
	<b>C5</b>	"The National Technology Roadmap for Semiconductors," (1997) Semiconductor Industry Association	

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation is not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \*Applicant is to place a check mark here if English language translation is attached.